

ABSTRACT OF THE DISCLOSURE

A speed control means 7 for outputting a drive command signal for a motor 1 to make the speed of the motor 1 conform to the speed command signal based on a signal indicating the difference
5 between the speed command signal and the detected speed of the motor; a filter 13 inserted in series with the speed control means 7 in the speed control loop, having a high frequency domain, a low frequency domain, and an intermediate frequency domain between the high frequency domain and the low frequency domain, a gain K_L in
10 the low frequency domain being larger than a gain K_H in the high frequency domain, and having a phase lag characteristic so that the phase in the intermediate frequency domain is delayed; the speed control means 7 having a proportional controller 9 that multiplies input by a proportional gain K_P and outputs the result, and the filter
15 13 being set so that phase lag occurs between a resonance frequency and an anti-resonance frequency of a mechanical system.